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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Kimikatsu SHOJI et al.

Appl. No.: 09/377,740

Confirmation No.: 8263

Filed: August 20, 1999

For: Semiconductor Integrated Circuit
and Method of Manufacturing the
Same

Art Unit: 3634

Examiner: Sara Crane

Atty. Docket No. 32307-150502

Customer No.: 26694
PATENT TRADEMARK OFFICE

Request for Corrected Notice of Recordation of Assignment Document

Commissioner for Patents
P.O. Box 1450
Alexandria, VA. 22313-1450

Attention: ASSIGNMENT DIVISION

Sir:

Submitted herewith is a marked-up copy of the Notice of Recordation of Assignment Document with a request that the Assignee address read --7-12, Toranomom 1-chome, Minato-ku, Tokyo, Japan --as reflected in the attached assignment.

No fee is enclosed as it appears that the error was made by the United States Patent and Trademark Office. If a fee is due, please charge Deposit Account No. 22-0261.

Respectfully submitted,

Date: 05/19/04

Michael A. Sartori, Ph.D.
Registration No. 41,289
Chad C. Anderson
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VENABLE LLP
P.O. Box 34385
Washington, D.C. 20043-9998
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FEBRUARY 16, 2000



**UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office**

ASSISTANT SECRETARY AND COMMISSIONER
OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

PTAS



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32014 150502

VENABLE
ROBERT J. FRANK
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WASHINGTON, D.C. 20043-9998

**UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT**

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 11/16/1999

REEL/FRAME: 010382/0446
NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

SHOJI, KIMIKATSU

DOC DATE: 09/08/1999

ASSIGNOR:

TADOKORO, HIROFUMI

DOC DATE: 09/07/1999

ASSIGNOR:

YANAGA, OSAMU

DOC DATE: 09/08/1999

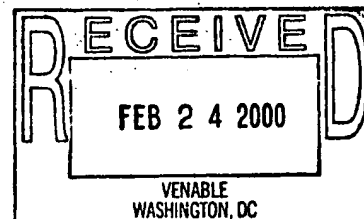
ASSIGNEE:

OKI ELECTRIC INDUSTRY CO., LTD.

[7-17, TORANOMON 1-CHOME -- 7-12, TORANOMON 1-CHOME
MINATO-KU, TOKYO, JAPAN] MINATO-KU, TOKYO, JAPAN --

SERIAL NUMBER: 09377740
PATENT NUMBER:

FILING DATE: 08/20/1999
ISSUE DATE:



010382/0446 PAGE 2

TARA WASHINGTON, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS



VENABLE, BAETJER, HOWARD & CIVILETTI, LLP
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11-19-1999



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TO THE ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

Attorney Docket: 32014-150502

REQUEST FOR RECORDING AND COVER SHEET UNDER 37 C.F.R. 3.28 AND 3.31

Please record the attached original document.

1. Name of party conveying the interest:

Kimikatsu SHOJI; Hirofumi TADOKORO; and Osamu YANAGA

2. Name and address of party receiving the interest:

OKI ELECTRIC INDUSTRY CO., LTD.
7-12, Toranomom 1-chome
Minato-ku, Tokyo, JAPAN

3. Description of the interest conveyed or transaction to be recorded:

☒ Assignment ☐ Other:

4. Application Number: 09/377,740

☐ Document filed together with a patent application executed on _____

5. Address correspondence concerning this request to: VENABLE
P.O. Box 34385, Washington, D.C. 20043-9998.

6. Number of Applications, Patents or Registrations and total fee:

Patents: one patent right @ \$40 Total: \$40.00

7. Date of execution of document: September 8, 1999; September 7, 1999; and September 8, 1999

8. If document is an assignment of a trademark right to an assignee who is not domiciled in the United States, such assignee has designated a domestic representative

☐ per attached designation ☐ other:

9. To the best of knowledge and belief of the undersigned, the foregoing information is true and correct and any attached copy is a true copy of the original document.

10. Date: November 16, 1999

Robert J. Frank

Name: Robert J. Frank

Registration Number: 19,112

RJF/njp

11/19/1999 TTRAH 00000013 09377740 40.00 0P 04 FD:581

ASSIGNMENT
OF APPLICATION FOR UNITED STATES LETTERS PATENT

Attorney Docket: 32014-150502 OKI

WHEREAS Kimikatsu SHOJI; Hirofumi TADOKORO and Osamu YANAGA, all of Tokyo, Japan hereinafter (collectively) referred to as the assignor, has invented a certain improvement relating to SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF MANUFACTURING THE SAME for which said assignor has caused an application for United States Letters Patent to be prepared, [] the inventor declaration for said application being executed concurrently with the execution of this instrument; said application to be filed in the United States Patent and Trademark Office; [X] said application having been filed in the United States Patent and Trademark Office on August 20, 1999, Serial Number (if known) _____; [] said application having been filed under the Patent Cooperation Treaty on _____, Serial No. _____, the United States of America being designated.

AND WHEREAS:

OKI ELECTRIC INDUSTRY CO., LTD.
7-12, Toranomon 1-chome, Minato-ku, Tokyo
JAPAN

hereinafter referred to as the assignee, is desirous of acquiring the entire right, title and interest in and to said application, including any and all divisions and continuations thereof, and in and to said invention and any and all patents which may be granted therefor, including any and all renewals, reissues and prolongations thereof;

NOW THIS WITNESSETH, that for and in consideration of One Dollar (\$1.00), and other good and valuable consideration paid by said assignee to said assignor, the receipt of which is hereby acknowledged, said assignor hereby assigns, sells and transfers to said assignee, and said assignee's successors and assigns the entire and exclusive right, title and interest in and to said application, including any and all divisions and continuations thereof, and in and to said invention and any and all patents which may be granted therefor, including any and all renewals, reissues and prolongations thereof; said assignee, and said assignee's successors and assigns, to have, hold, exercise and enjoy the said application, including any and all divisions and continuations thereof, and the said invention and any and all patents which may be granted therefor, including any and all renewals, reissues and prolongations thereof, with all the rights, powers, privileges and advantages in anywise arising from or appertaining hereto, for and during the term or terms of any and all such patents when granted, including any and all renewals, reissues and prolongations thereof, for the use and benefit of said assignee, and said assignee's successors and assigns, in as ample and beneficial a manner as the said assignor might or could have held and enjoyed the same, if this assignment had not been made.

AND said assignor hereby agrees to perform, upon the request of said assignee, or said assignee's successors or assigns, any and all acts relating to the obtaining or to the asserting of said patents, including any and all renewals, reissues and prolongations thereof.

AND said assignor authorizes and requests the Commissioner of Patents and Trademarks to issue Letters Patent on said application, and on any and all divisions and continuations thereof, to said assignee, and said assignee's successors and assigns, in accordance herewith.

EXECUTED, this 8th day of September, 1999

WITNESSES:

Signature: Yukio UematsuName: Yukio UEMATSUAddress: c/o Oki Electric Industry Co., Ltd.
7-12, Toranomon 1-chome, Minato-ku
Tokyo, JAPAN

Signature: _____

Name: _____

Address: _____

ASSIGNOR:

Signature: Kimikatsu Shoji

Name: Kimikatsu SHOJI

I hereby certify that the Patent and Trademark Office has advised that the application identified herein has been assigned Application No. 09/377,740..

Date: 11-16-99

Robert J. Frank
Robert J. Frank (Reg. No. 19,112)

ASSIGNMENT
OF APPLICATION FOR UNITED STATES LETTERS PATENT

Attorney Docket: 32014-150502 OKI

Title: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF MANUFACTURING THE SAME

EXECUTED, this 7th day of September, 19 99.

WITNESSES:

Signature: Ikuo Matsuhashi

Name: Ikuo MATSUHASHI

Address: c/o Oki Electric Industry Co., Ltd.
7-12, Toranomon 1-chome, Minato-ku
Tokyo, JAPAN

ASSIGNOR:

Signature: Hirofumi Tadokoro

Name: Hirofumi TADOKORO

Signature: _____

Name: _____

Address: _____

EXECUTED, this 8th day of September, 19 99.

WITNESSES:

Signature: Atsushi Masuda

Name: Atsushi MASUDA

Address: c/o Oki Electric Industry Co., Ltd.
7-12, Toranomon 1-chome, Minato-ku
Tokyo, JAPAN

ASSIGNOR:

Signature: Osamu Yanaga

Name: Osamu YANAGA

Signature: _____

Name: _____

Address: _____